

Vbase is calculated as follows:-

$$V_{base} = \frac{V_{in} * Gain}{(56K + 1K + 42R2)} \times (1K + 42R2) = 18.77mV \text{ for a } 1V \text{ input and unity gain}$$

The range afforded by P157 is (assuming 100K input resistor), iss,

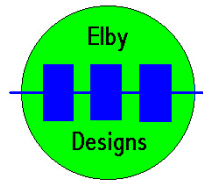
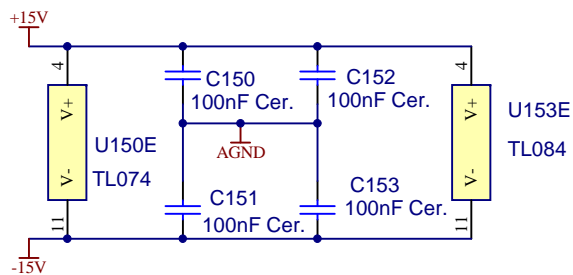
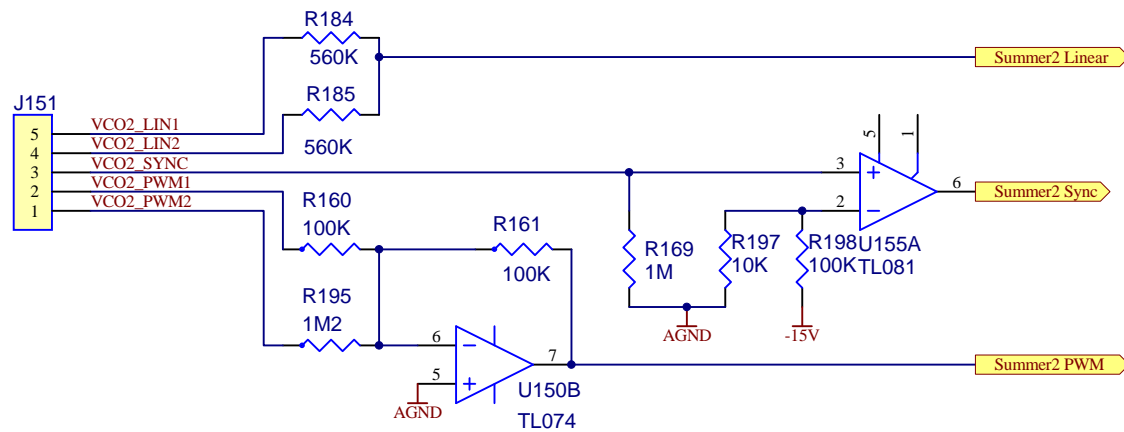
P157	Gain	Vbase
0K (minimum)	0.931	17.0mV
10K (maximum)	1.031	18.8mV

This puts 18mV near the centre of the trim range.

CREDITS:

Terry Mikulic, "New Design Ideas for Voltage-Controlled Oscillators" Electronotes, Vol. 8, No. 62, Feb. 1976, pp 13-15

Bernie Hutchins, "The ENS-76 Home-Built Synthesizer System - Part 7, VCO Options" Electronotes, Vol. 9, No. 75, March 1977, pp 3-10



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Australia

ASM2 - Voltage Controlled Oscillator (2)

Summer

Size A4	FCSM No. 21/08/2010	DWG No. vco2-summer.schdoc	Rev C
Scale	Laurie Biddulph	Sheet 23 of 24	